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APPLICANTS							
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** CONTINUING D	ATA *********	•					
This applicat	ion is a CON of PCT/JP03/	/00472 01/21/2003					
** FORFIGN APPL	ICATIONS *************	***					
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TITLE				-			
Semiconductor devi	ce having Heterojunction ty	pe MIS transistor which	ch can operate	at reduci	ed vo	oltage while	
maintaining high ope	eration speed						
			☐ All F	☐ All Fees			
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